

Amendments to the Specification:

Please replace the title with the following amended title:

SWITCH CONTROL SYSTEM AND METHOD FOR A PLURALITY OF
~~INPUT DEVICES THAT DISTINGUISHES BETWEEN A PLURALITY OF~~
5 REAL AND EMULATED INPUT DEVICES

All of the following paragraph numbers refer to the specification as published.

Please replace Paragraph [0005] with the following amended paragraph:

[0005] Please refer to FIG. 1, which is a block diagram of a switch control system for a plurality of keyboard/mouse sets according to the prior art. The switch control system
10 comprises a server 100, a real keyboard/mouse set 104, an emulation keyboard/mouse set 106, and a switch device 102 ~~connected~~ (connected to the server 100, the real keyboard/mouse set 104 and the emulation keyboard/mouse set ~~106~~ 106). The switch is manually switched to connect the server 100 to the real keyboard/mouse set 104 or to the emulation keyboard/mouse set 106.

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Please replace Paragraph [0006] with the following amended paragraph:

[0006] However, the manual switching mechanism between the real keyboard/mouse set 104 and the emulation keyboard/mouse set 106 is inconvenient. Moreover, since the real
20 keyboard/mouse set 104 and the emulation keyboard/mouse set 106 usually transmit data by making use of a PS/2 interface, a bi-directional transmission interface. The server 100 does not stop transmitting a control signal to the real keyboard/mouse set 104 while the switch device 102 is switched to connect the server 100 with the emulation keyboard/mouse set 106. A signal collision problem resulting from a real signal from the
25 real keyboard/mouse set 104 colliding with an ~~emulation~~ emulated signal from the

emulation keyboard/mouse set 106 becomes inevitable.

Please replace Paragraph [0009] with the following amended paragraph:

- 5 [0009] It is another objective of the claimed invention to provide a switch control system and a related switch control method for controlling a computer to transmit/receive real signals of a real input device, or to transmit/receive ~~emulation~~ emulated signals of an emulation input device by the use of triggering a plurality of input devices.

- 10 **Please replace Paragraph [0012] with the following amended paragraph:**

- [0012] The instruction detecting device coupled to the computer system and the switch device detects the clock/data signal transmitted from the computer system to the switch device. The instruction detecting device outputs a first detecting signal when a first
15 acknowledge signal of the real input device responsive to the computer system is detected. The instruction detecting device outputs a second detecting signal D.sub.2 instead of the first detecting signal when ~~OLE_LINK1~~ a a second acknowledge signal of the emulation input device responsive to the computer system is ~~detected~~. OLE_LINK1 detected. The first detecting signal and the second detecting signal serve to determine the clock/data
20 signals' transmission between the switch and the computer system.

Please replace Paragraph [0025] with the following amended paragraph:

- [0025] The present invention discloses a switch control system and a related method for a
25 plurality of input devices. A switch device is controlled to manage data transmission between a computer system and a real input device or an emulation input device, solving the signal collision problem resulting from the real signals colliding with the ~~emulation~~ emulated signals. The switch device is switched to transfer real signals between the

computer system and the real input device or to transfer ~~emulation~~ emulated signals between the computer system and the emulation input device by the use of triggering a plurality of switches.

5 **Please replace Paragraph [0028] with the following amended paragraph:**

[0028] The instruction detecting device 204 is coupled to the computer system 208 and the switch device 202 for detecting the clock/data signal transmitted from the computer system 208 to the switch device 202. The instruction detecting device 204 outputs a first
10 detecting signal D.sub.1 when a first acknowledge signal (~~ACK~~) A of the real input device 210 responsive to the computer system 208 is detected. The instruction detecting device 204 outputs a second detecting signal D.sub.2 instead of the first detecting signal D when a second acknowledge signal A.sub.2 of the emulation input device 212
15 responsive to the computer system 208 is detected. The first detecting signal D.sub.1 and the second detecting signal D.sub.2 serve to determine the clock/data signals' transmission between the switch 202 and the computer system 208.

Please replace Paragraph [0030] with the following amended paragraph:

20 [0030] Please note that the switch control system 200 of the present invention further comprises a logic device 214 and a setting device 216. The logic device 214 comprises a first input terminal 218 coupled to the control device 206 for receiving the control signal Cn, a second input terminal 220, and an output terminal 222 coupled to the switch device 202 for outputting the control signal Cn to the switch device 202. The setting device 216
25 coupled to the second input terminal 220 of the logic device 214 is used to set a plurality of operating modes of the logic device 214 and to trigger the switch device 202 with the control signal Cn. According to the preferred embodiment, the logic device 214 comprises an AND "AND" gate, an OR "OR" gate or a combination of other logic components.

Please replace Paragraph [0031] with the following amended paragraph:

[0031] Please refer to FIG. 3, which is a block diagram of the switch device 202 of the
5 switch control system 200 shown in FIG. 2 according to the present invention. A
clock/data signal comprises a real clock/data signal and an emulation clock/data signal.
The switch device 202 comprises a first switch 224, a second switch 226, a third switch
228 and a fourth switch 230. The first switch 224 is coupled to the real input device 210
for conducting the real clock/data signal. The second switch 226 is coupled to the first
10 switch 224, the emulation input device 212, the control device 206 and the computer
system, respectively, 208 for conducting and transmitting either the real clock/data signal
or the emulation clock/data signal to the computer system 208 according to the control
signal Cn. The third switch 228 is coupled to the computer system 208, the emulation
input device 212 and the ~~first switch 224~~ real input device 210 for conducting and
15 transmitting the clock/data signal from the computer system 208 either to the real input
device 210 or to the emulation input device 212 according to the control signal Cn. The
fourth switch 230 coupled to the first switch 224, the third switch 228 and the real input
device 210, respectively, for the emulation input device 212 to receive either the
emulation clock/data signal from the computer system 208 or the real clock/data signal
20 from the third switch 228 by disabling the first switch 224 according to the control signal
Cn. In the preferred embodiment, a predetermined voltage level signal [[b]] b1 is a
high-level voltage or a low-level voltage can be provided to second inputs of the first,
third, and fourth switches according to design considerations.

25 **Please replace Paragraph [0036] with the following amended paragraph:**

[0036] In such a scenario as described above, the signal collision problem is therefore
resolved and the computer system 208 can receive/transmit data correctly when the

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computer system 208 is receiving/transmitting data of the real input device 210 or the emulation input device 212.